

FE-NANO SIP - PRODUCT DATASHEET

At a Glance

- SoC – Orange Pi Zero, Allwinner H2
- CPU – H2 Quad-core Cortex-A7 H.265/HEVC 1080p
- GPU – Mali400MP2 GPU @600MHz
- RAM – 256MB/512MB DDR3 SDRAM
- Network – 10/100 Ethernet, 2.4GHz 802.11n wireless
- Storage – microSD
- Power DC 5V DC-IN via microUSB or pin headers or PoE (optional)
- GPIO – 26 pins header, compatible with Raspberry Pi B+
- Ports – CVBS (on pin headers), microphone, stereo line-out on pin headers, 1 USB 2.0 Host



TECHNICAL SPECIFICATION	
HARDWARE	
CPU	H2 Quad-core Cortex-A7 H.265/HEVC 1080p
GPU	Mali 400MP2 GPU @600MHz, supports OpenGL ES 2.0
Memory (SDRAM)	256MB/512MB DDR SDRAM
Onboard Storage	TF card (Max. 32GB)/ SPI flash
Onboard Network	10/100M ethernet RJ45 PoE is off by default
Onboard WIFI	XR819, IEEE 802.11 b/g/n
Audio Input	MIC
Video Outputs	Supports external board with 13 pins
Power Source	USB OTG can supply power
USB 2.0 Ports	Only one USB 2.0 HOST, one USB 2.0 OTG
Button	Power Button
Low-level Peripherals	26 Pins Header, compatible with Raspberry Pi B+, 13 Pins Header, with 2x USB, IR pin, AUDIO(MIC, AV)
LED	Power LED and Status LED
Supported OS	Android, Lubuntu, Debian, Raspbian
Product size	48 x 46mm
Weight	26g

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WE HAVE MORE THAN 30 YEARS OF EXPERIENCE IN THIS TECHNOLOGY FIELD AS DEVELOPER, MANUFACTURER AND RESALER COMPANY.

IN THE INDUSTRY, WE NEGOTIATE THE MOST INNOVATIVE SOLUTIONS WITHIN BUILDING COMMUNICATION. WE HAVE DAILY FOCUS ON DEVELOPMENT AND USER-FRIENDLINESS AND OUR PRODUCTS HAVE TECHNICAL HIGH QUALITY AND PLEASANT DESIGN.

AS A DEVELOPER AND MANUFACTURER OF OUR OWN IP-INTEGRA SYSTEMS, WE ARE ON TOP OF THE MOST ADVANCED SYSTEMS FOR DOOR TELEPHONY, PUBLIC AUDIO AND ACCESS CONTROL SOLUTIONS.

OUR DEVELOPMENT DEPARTMENT HAVE DEVELOPED, TOGETHER WITH OUR PARTNERS, ELEGANT AND ROBUST DOORPHONES, SIP-CENTRALS, TERMINALS, IP-SPEAKERS, ACCESS CONTROLLERS AND APPLICATIONS WITH INTELLIGENT FEATURES AND THE MOST ADVANCED TECHNOLOGY, BUT AT THE SAME TIME EASY FOR OUR CUSTOMERS TO USE.

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